

Guide to Harmonics with AC Drives



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Chapter 1 - Introduction

General

This guide continues ABB's technical guide series, describing harmonic distortion, its sources and effects, and also distortion calculation and evaluation. Special attention has been given to the methods for reducing harmonics with AC drives.

Chapter 2 - Basics of the harmonics phenomena

Harmonic currents and voltages are created by non-linear loads connected on the power distribution system. Harmonic distortion is a form of pollution in the electric plant that can cause problems if the sum of the harmonic currents increases above certain limits.

All power electronic converters used in different types of electronic systems can increase harmonic disturbances by injecting harmonic currents directly into the grid. Figure 2.1 shows how the current harmonics (i_h) in the input current (i_s) of a power electronic converter affect the supply voltage (u_t).

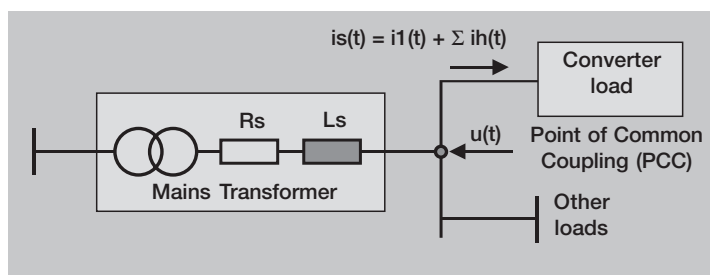


Figure 2.1 Plant with converter load, mains transformer and other loads

The line current of a rectifier can be calculated from the direct output current by using the following formula.

$$I_1 = \sqrt{\frac{2}{3}} * I_d, \text{ where}$$

I_1 = the fundamental line current and

I_d = direct current output from the rectifier.

In a theoretical case where output current can be estimated as clean dc current, the harmonic current frequencies of a 6-pulse three phase rectifier are n times the fundamental frequency (50 or 60 Hz). The order numbers n are calculated from the formula below:

$$n = 6k \pm 1, \text{ where } k = 1, 2, 3, \dots$$

The rms values of the harmonic components are:

$$I_{ni} = \frac{I_1}{n}$$

and the harmonic components are as shown in Figure 2.2.

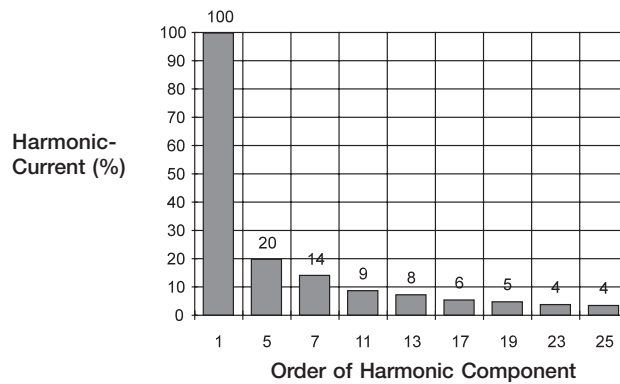


Figure 2.2 The harmonic content in the rectangular current of a 6-pulse rectifier

The principle of how the harmonic components are added to the fundamental current is shown in Figure 2.3, where only the 5th harmonic is shown.

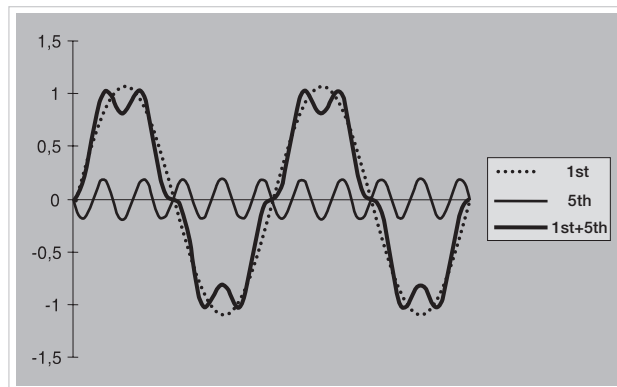


Figure 2.3 The total current as the sum of the fundamental and 5th harmonic

Chapter 3 - Harmonic distortion sources and effects

Common non-linear loads include motor starters, variable speed drives, computers and other electronic devices, electronic lighting, welding supplies and uninterrupted power supplies.

The effects of harmonics can be overheating of transformers, cables, motors, generators and capacitors connected to the same power supply with the devices generating the harmonics. Electronic displays and lighting may flicker, circuit breakers can trip, computers may fail and metering can give false readings.

If the cause of the above mentioned symptoms is not known, then there is cause to investigate the harmonic distortion of the electricity distribution at the plant. The effects are likely to show up in the customer's plant before they show on the utility system. This Technical Guide has been published to help customers to understand the possible harmonic problems and make sure the harmonic distortion levels are not excessive.

Chapter 4 - Harmonic distortion calculation example

The harmonic currents cause a distortion of the line voltage. In principle the voltage harmonics can be calculated at any point of the network if the harmonic currents and the corresponding source impedances are known. The circuit diagrams in Figure 4.1 show the network supplying the converter and the other essential parts of the installation. The electrical data for the installation needed for the harmonics calculation is also given in the figure. The steps for the calculation and the results are given as an example in sections 4.1 to 4.6.

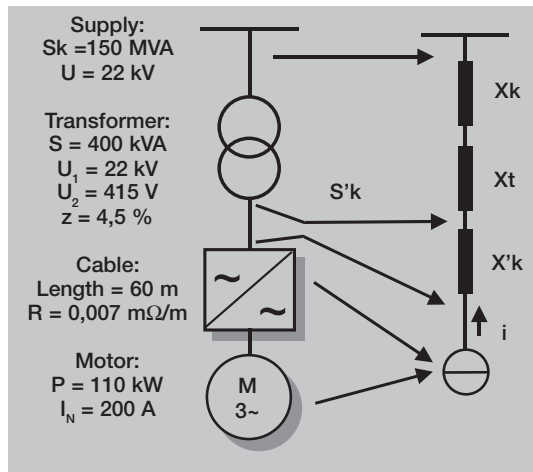


Figure 4.1 Network supplying a frequency converter on the left and its equivalent diagram on the right

4.1 Calculation of the source impedances and short circuit power

The short circuit impedance of the supply is reduced to the 415 V side:

$$X_k = \frac{(415V)^2}{150 \text{ MVA}} = 1,15 \text{ m}\Omega$$

The short circuit impedance of the supply transformer:

$$X_t = \frac{4,5}{100} * \frac{(415V)^2}{400 \text{ kVA}} = 19,4 \text{ m}\Omega$$

The impedance of supply cables between transformer and converter:

$$X'_k = 60 * 0,007 \text{ m}\Omega = 0,42 \text{ m}\Omega$$

The short circuit power at the converter terminals:

$$S'_k = \frac{(415V)^2}{X_k + X_t + X'_k} = 8,2 \text{ MVA}$$

4.2 Calculation of the effect of commutation

The effect of commutation with a 6-pulse rectifier:

$$\mu_0 = \arccos\left(1 - \frac{\sqrt{3} * I * U}{S'_k}\right) = \arccos\left(1 - \frac{\sqrt{3} * 200A * 415V}{8,2MVA}\right) = 11^\circ$$

The effect of commutation with a 12-pulse rectifier:

$$\mu_0 = \arccos\left(1 - \frac{\sqrt{3} * I * U}{S'_k}\right) = \arccos\left(1 - \frac{\sqrt{3} * 100A * 415V}{8,2MVA}\right) = 8^\circ$$

The commutation correction coefficients are determined from the angles. They are used to multiply the theoretical harmonics.

4.3 Table: Correction coefficients

n	6-pulse rectifier Correction coefficient	12-pulse rectifier Correction coefficient
5	0,98	0,99
7	0,96	0,97
11	0,90	0,93
13	0,86	0,90
17	0,78	0,87
19	0,72	0,93
23	0,62	0,77
25	0,52	0,73

4.4 Calculation of voltage distortion

The harmonic current I_n causes a harmonic voltage component U_n proportional to the fundamental line voltage U_1 at the supply busbars of the converter.

$$\frac{U_n}{U_1} = \frac{\sqrt{3} * I_n * U_1 * n}{S'_k}$$

The total harmonic distortion can be estimated by calculating the sum of a sufficient number of harmonics (usually 25 or 40).

$$THD = \frac{\sqrt{\sum_{n=2}^{\infty} U_n^2}}{U_1}$$

4.5 Table:
Harmonic
currents and
voltages in
22 kV side:

$$I_1 = 415 \text{ V} / 22 \text{ kV} * 200 \text{ A} = 3,77 \text{ A}$$

	6-pulse bridge			12-pulse bridge	
n	I_n/I_1 (%)	U_n/U_1 (%)	n	I_n/I_1 (%)	U_n/U_1 (%)
5	19,60	0,09	5		
7	13,71	0,09	7		
11	8,18	0,09	11	8,45	0,09
13	6,62	0,08	13	6,92	0,09
17	4,59	0,07	17		
19	3,79	0,07	19		
23	2,70	0,06	23	3,35	0,07
25	2,24	0,05	25	2,92	0,07
		6-pulse bridge	12-pulse bridge		
Total current distortion		27,3 %	11,80 %		
Total voltage distortion		0,22 %	0,16 %		

4.6 Table:
Harmonic
currents and
voltages in
415 V side:

$$I_1 = 200 \text{ A}$$

	6-pulse bridge			12-pulse bridge	
n	I_n/I_1 (%)	U_n/U_1 (%)	n	I_n/I_1 (%)	U_n/U_1 (%)
5	19,60	1,71	5		
7	13,71	1,68	7		
11	8,18	1,57	11	8,45	1,63
13	6,62	1,50	13	6,92	1,57
17	4,59	1,36	17		
19	3,79	1,26	19		
23	2,70	1,08	23	3,35	1,35
25	2,24	0,98	25	2,92	1,28
		6-pulse bridge	12-pulse bridge		
Total current distortion		27,3 %	11,80 %		
Total voltage distortion		4,01 %	2,93 %		

Chapter 5 - Standards for harmonic limits

The most common international and national standards setting limits on harmonics are described below.

Figure 5.1 is shown as an example for harmonic distortion limits.

5.1 EN61800-3 (IEC 1800-3) Adjustable speed electrical power drive systems

Part 3: EMC product standard including specific test methods

The countries of the European Economic Area (EEA) have agreed on common minimum regulatory requirements in order to ensure the free movement of products within the EEA. The CE marking indicates that the product works in conformity with the directives that are valid for the product. The directives state the principles that must be followed. Standards specify the requirements that must be met. EN61800-3 is the EMC product standard of adjustable speed electrical power drive systems (PDS). Meeting the requirements of this standard, is the minimum condition for free trade of power electronics converters inside the EEA.

EN61800-3 states, that the manufacturer shall provide in the documentation of the PDS, or on request, the current harmonic level, under rated conditions, as a percentage of the rated fundamental current on the power port. The referenced values shall be calculated for each order at least up to the 25th. The current THD (orders up to and including 40), and its high-frequency component PHD (orders from 14 to 40 inclusive) shall be evaluated. For these standard calculations, the PDS shall be assumed to be connected to a PC with $R_{sc} = 250$ and with initial voltage distortion less than 1%. The internal impedance of the network shall be assumed to be a pure reactance.

In a low voltage public supply network, the limits and requirements of IEC 1000-3-2 apply for equipment with rated current ≤ 16 A. The use of the future IEC 1000-3-4 is recommended for equipment with rated current > 16 A. If PDS is used in an industrial installation, a reasonable economical approach, which considers the total installation, shall be used. This approach is based on the agreed power, which the supply can deliver at any time. The method for calculating the harmonics of the total installation is agreed and the limits for either the voltage distortion or the total harmonic current emission are agreed on. The compatibility limits given in IEC1000-2-4 may be used as the limits of voltage distortion.

5.2
IEC1000-2-2,
Electromagnetic
compatibility
(EMC)

Part 2: Environment - Section 2: Compatibility levels for low frequency conducted disturbances and signalling in public low-voltage power supply systems

This standard sets the compatibility limits for low-frequency conducted disturbances and signalling in **public low-voltage power supply** systems. The disturbance phenomena include harmonics, inter-harmonics, voltage fluctuations, voltage dips and short interruptions voltage imbalance and so on. Basically this standard sets the design criteria for the equipment manufacturer, and amounts to the minimum immunity requirements of the equipment. IEC1000-2-2 is in line with the limits set in EN50160 for the quality of the voltage the utility owner must provide at the customer's supply-terminals.

5.3
IEC1000-2-4,
Electromagnetic
compatibility
(EMC)

Part 2: Environment - Section 4: Compatibility levels in industrial plants for low frequency conducted disturbances

IEC1000-2-4 is similar to IEC1000-2-2, but it gives compatibility levels **for industrial and non-public networks**. It covers low-voltage networks as well as medium voltage supplies excluding the networks for ships, aircraft, offshore platforms and railways.

5.4
IEC1000-3-2,
Electromagnetic
compatibility
(EMC)

Part 3: Limits - Section 2: Limits for harmonic current emissions (equipment current <16 A per phase)

This standard deals with the harmonic current emission limits of individual equipment connected to **public networks**. The date of implementation of this standard is January 1st 2001, but there is extensive work going on at the moment to revise the standard before this date. The two main reasons for the revision are the need for the standard to cover also the voltage below 230 V and the difficulties and contradictions in applying the categorisation of the equipment given in the standard.

5.5
IEC1000-3-4,
Electromagnetic
compatibility
(EMC)

This standard has been published as a Type II Technical report. Work is going on to convert it into a standard. It gives the harmonic current emission limits for individual equipment having a rated current of more than 16 A up to 75 A. It applies to public networks having nominal voltages from 230 V single phase to 600 V three phase.

The standard gives three different stages for connection procedures of the equipment. Meeting the individual harmonic limits of Stage 1 allows the connection of the equipment at any point in the supply system. Stage 2 gives individual harmonic current limits as well as THD and its weighted high frequency counterpart PWHD. The limits are classified and tabulated by the short circuit ratio. The third stage of connection is based on an agreement between the user and the supply authority, based on the agreed active power of the consumer's installation. If the rated current is above 75 A, Stage 3 applies in any case.

The structure of this standard is generally seen to be good, but it may justly be questioned whether single and three-phase equipment should have different limits in Stage 2. It is very probable that the structure of the standard will remain as it is, but the version having the status of actual standard, will contain different limits for single and three-phase equipment.

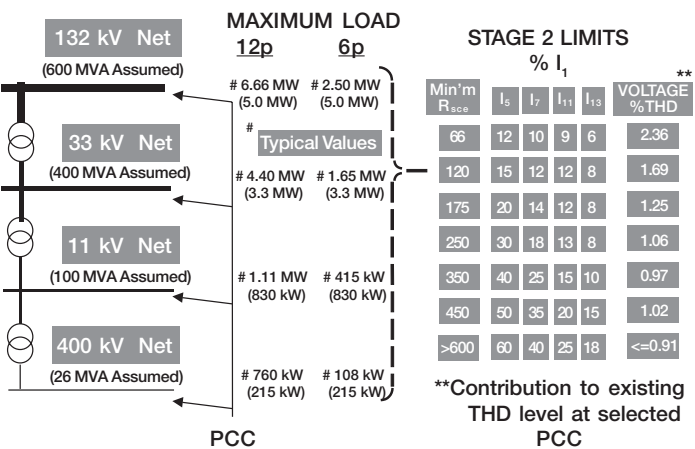


Figure 5.1 Limits on Harmonics in the proposed EN61000-3-4

5.6 IEEE 519, IEEE Recommended practices and requirements for harmonic control in electrical power systems

The philosophy of developing harmonic limits in this recommended practice is to limit the harmonic injection from individual customers so that they will not cause unacceptable voltage distortion levels for normal system characteristics and to limit overall harmonic distortion of the system voltage supplied by the utility. This standard is also recognised as American National Standard and it is widely used in the USA, especially in the municipal public works market.

The standard does not give limits for individual equipment, but for individual customers. The customers are categorised by the ratio of available short circuit current (I_{sc}) to their maximum demand load current (I_L) at the point of common coupling. The total demand load current is the sum of both linear and non-linear loads. Within an industrial plant, the PCC is clearly defined as the point between the non-linear load and other loads.

The allowed individual harmonic currents and total harmonic distortion are tabulated by the ratio of available short circuit current to the total demand load current (I_{sc}/I_L) at the point of common coupling. The limits are as a percentage of I_L for all odd and even harmonics from 2 to infinity. Total harmonic distortion is called total demand distortion and also it should be calculated up to infinity. Many authors limit the calculation of both the individual components and TDD to 50.

The table 10.3 of the standard is sometimes misinterpreted to give limits for the harmonic emissions of a single apparatus by using R_{sc} of the equipment instead of I_{sc}/I_L of the whole installation. The limits of the table should not be used this way, since the ratio of the short circuit current to the total demand load current of an installation should always be used.

Chapter 6 - Evaluating harmonics

The "Guide for Applying Harmonic Limits on Power Systems" P519A/D6 Jan 1999 introduces some general rules for evaluating harmonic limits at an industrial facility. The procedure is shown in the flowchart in Figure 6.1.

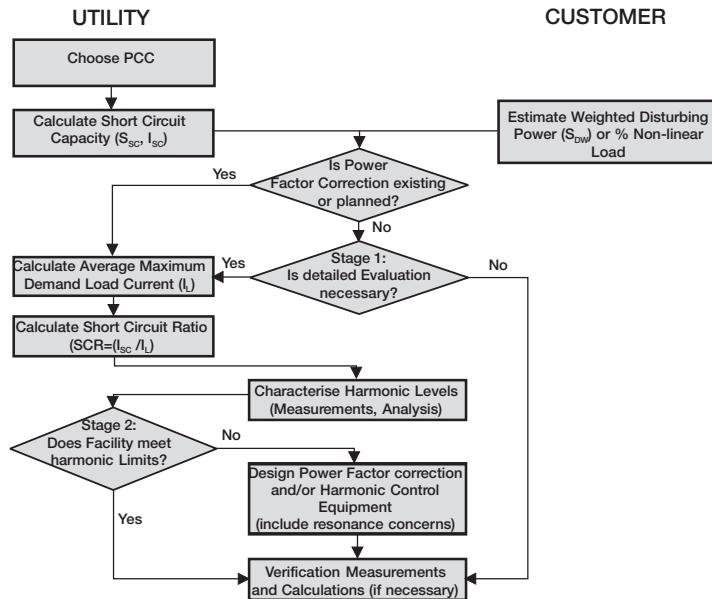


Figure 6.1 Evaluation of harmonic distortion

Chapter 7 - How to reduce harmonics by structural modifications in the AC drive system

Harmonics reduction can be done either by structural modifications in the drive system or by using external filtering. The structural modifications can be to strengthen the supply, to use 12 or more pulse drive, to use a controlled rectifier or to improve the internal filtering in the drive.

7.1 Factors in the AC drive system having an effect on harmonics

Figure 7.1 shows the factors in the AC drive system which have some influence on harmonics. The current harmonics depend on the drive construction and the voltage harmonics are the current harmonics multiplied by the supply impedances.

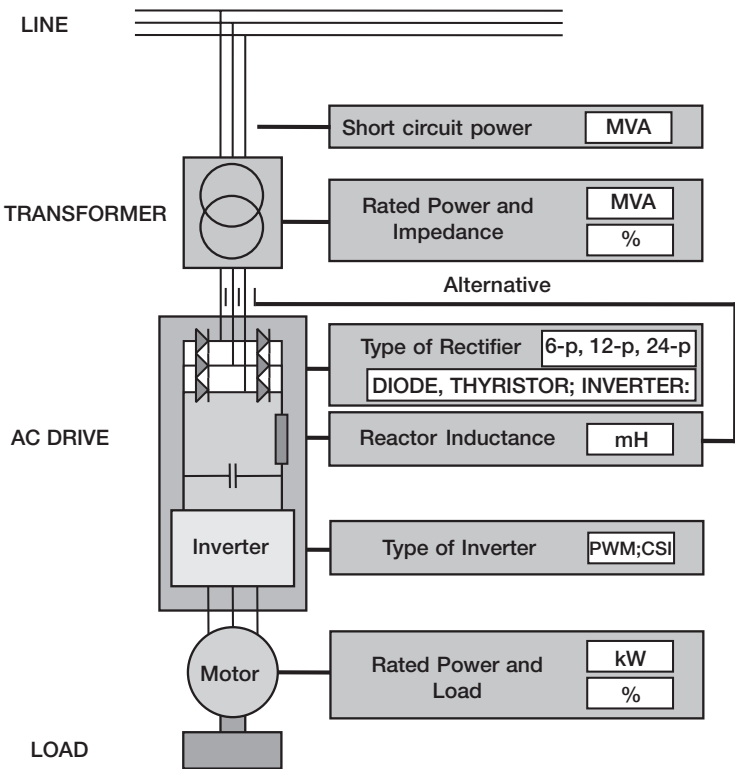


Figure 7.1 Drive system features affecting harmonics

**7.2 Table:
List of the
different
factors and
their effects**

The cause	The effect
The larger the motor...	the higher the current harmonics
The higher the motor load...	the higher the current harmonics
The larger the DC or AC inductance...	the lower the current harmonics
The higher the number of pulses in the rectifier...	the lower the current harmonics
The longer the supply cable...	the higher the voltage harmonics
The larger the transformer...	the lower the voltage harmonics
The higher the transformer impedance...	the lower the voltage harmonics
The higher the short circuit capacity of supply...	the lower the voltage harmonics

**7.3 Using
6-pulse diode
rectifier**

The connections for different rectifier solutions are shown in Figure 7.2. The most common rectifier circuit in 3-phase AC drives is a 6-pulse diode bridge. It consists of six uncontrollable rectifiers or diodes and an inductor, which together with a DC-capacitor forms a low-pass filter for smoothing the DC-current. The inductor can be on the DC- or AC-side or it can be left totally out. The 6-pulse rectifier is simple and cheap but it generates a high amount of low order harmonics 5th, 7th, 11th especially with small smoothing inductance.

The current form is shown in Figure 7.2. If the major part of the load consists of converters with a 6-pulse rectifier, the supply transformer needs to be oversized and meeting the requirements in standards may be difficult. Often some harmonics filtering is needed.

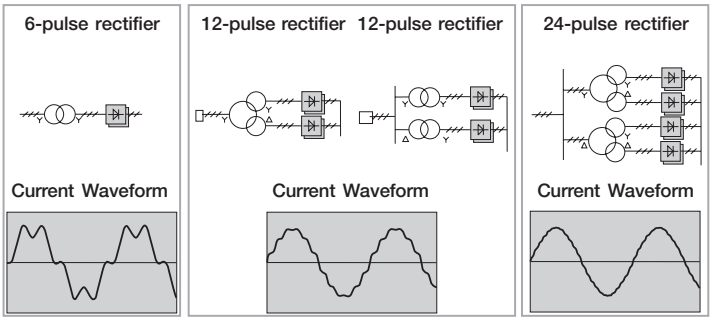


Figure 7.2 Harmonics in line current with different rectifier constructions

7.4 Using 12-pulse or 24-pulse diode rectifier

The 12-pulse rectifier is formed by connecting two 6-pulse rectifiers in parallel to feed a common DC-bus. The input to the rectifiers is provided with one three-winding transformer or two two-winding transformers. In both cases the transformer secondaries are in 30 ° phase shift. The benefit with this arrangement is that in the supply side some of the harmonics are in opposite phase and thus eliminated. In theory the harmonic component with the lowest frequency seen at the primary of the transformer is the 11th.

The major drawbacks are special transformers and a higher cost than with the 6-pulse rectifier.

The principle of the 24-pulse rectifier is also shown in Figure 7.3. It has two 12-pulse rectifiers in parallel with two three-winding transformers having primary windings with 30 ° phase shift. The benefit is that practically all low frequency harmonics are eliminated but the drawback is the high cost. In the case of a high power single drive or large multidrive installation a 24-pulse system may be the most economical solution with lowest harmonic distortion.

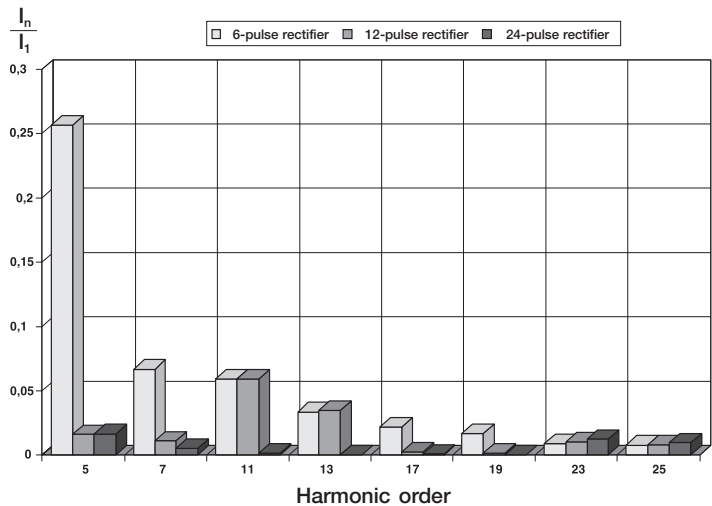


Figure 7.3 Harmonic components with different rectifiers

7.5 Using phase controlled thyristor rectifier

A phase controlled rectifier is accomplished by replacing the diodes in a 6-pulse rectifier with thyristors. Since a thyristor needs a triggering pulse for transition from nonconducting to conducting state, the phase angle at which the thyristor starts to conduct can be delayed. By delaying the firing angle over 90 °, the DC-bus voltage goes negative. This allows regenerative flow of power from the DC-bus back to the power supply.

Standard DC-bus and inverter configurations do not allow polarity change of the DC-voltage and it is more common to connect another thyristor bridge anti-parallel with the first one to allow the current polarity reversal. In this configuration the first bridge conducts in rectifying mode and the other in regenerating mode.

The current waveforms of phase controlled rectifiers are similar to those of the 6-pulse diode rectifier, but since they draw power with an alternating displacement power factor, the total power factor with partial load is quite poor. The poor power factor causes high apparent current and the absolute harmonic currents are higher than those with a diode rectifier.

In addition to these problems, phase-controlled converters cause commutation notches in the utility voltage waveform. The angular position of the notches varies along with the firing angle.

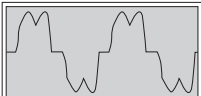
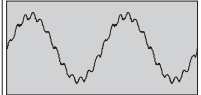
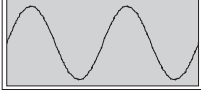
Supply type	Current TDH (%)	Voltage TDH (%) RSC=20	Voltage TDH (%) RSC=100	Current Waveform
6-pulse rectifier	30	10	2	
12-pulse rectifier	10	6	1.2	
IGBT Supply Unit	4	8	1.8	
Distortion is in % of RMS values				

Figure 7.4 Distortion of different supply unit types. Values may vary case by case.

7.6 Using IGBT bridge

Introducing a rectifier bridge, made of self commutated components, brings several benefits and opportunities compared to phase commutated ones. Like a phase commutated rectifier, this hardware allows both rectification and regeneration, but it makes it possible to control the DC-voltage level and displacement power factor separately regardless of the power flow direction.

The main benefits are:

- Safe function in case of mains supply disappearance.
- High dynamics of the drive control even in the field weakening range.

- Possibility to generate reactive power and even compensate harmonic currents of parallel loads.
- Nearly sinusoidal supply current with low harmonic content. Measured results for one drive is shown in Figure 7.5. When comparing with Figure 7.3 we can see a clear difference. IGBT has very low harmonics at lower frequencies, but somewhat higher at higher frequencies.
- Voltage boost capability. In case of low supply voltage the DC voltage can be boosted to keep motor voltage higher than supply voltage.

The main drawback is the high cost coming from the IGBT bridge and extra filtering needed.

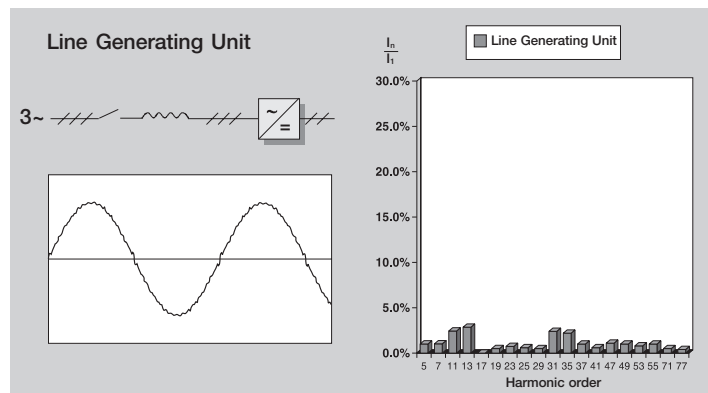


Figure 7.5 Harmonics in line current IGBT line generating unit

7.7 Using a larger DC or AC inductor

The harmonics of a voltage source AC drive can be significantly reduced by connecting a large enough inductor in its AC input or DC bus. The trend has been to reduce the size of converter while the inductor size has been also reduced, or in several cases it has been omitted totally. The effect of this can be seen from the curve forms in Figure 7.4.

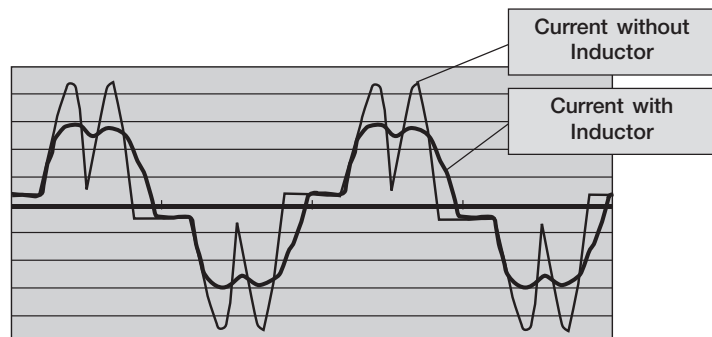


Figure 7.6 The effect of the inductor on the line current

The chart in Figure 7.5 shows the effect of size of the DC inductor on the harmonics. For the first 25 harmonic components the theoretical THD minimum is 29%. That value is practically reached when the inductance is 100 mH divided by the motor kW or 1 mH for a 100 kW motor (415 V, 50 Hz). No larger inductance should be selected, but about 25 mH divided by motor kW gives a THD of about 45%. This is 0,25 mH for a 100 kW motor.

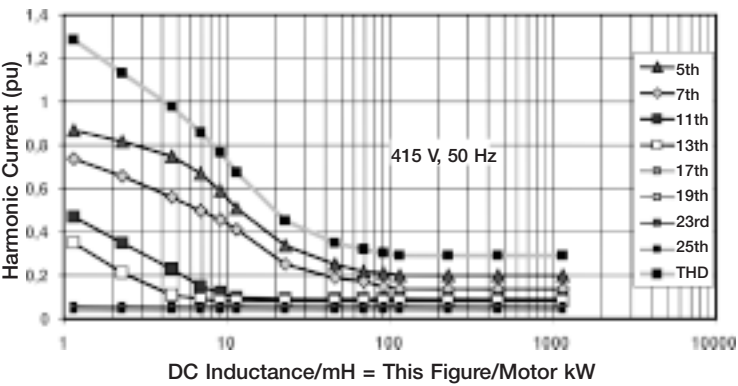


Figure 7.7 Harmonic current as function of DC inductance

The voltage distortion with certain current distortion depends on the Short Circuit Ratio Rsc of the supply. The higher the ratio, the lower the voltage distortion. This can be seen in Figure 7.8.

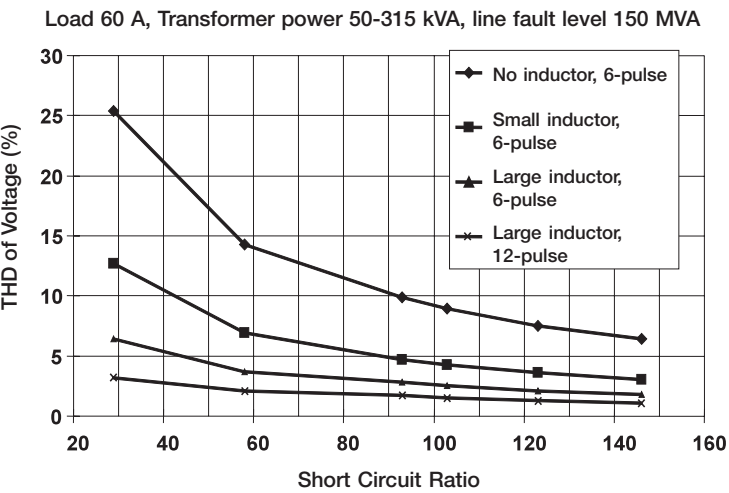


Figure 7.8 THD Voltage vs Type of AC drive and transformer size

Figure 7.9 introduces a simple nomogram for estimation of harmonic voltages. On the graph below right select first the motor kilowatt, then the transformer kVA and then move horizontally to the diagonal line where you move upwards and stop at the curve valid for your application. Then turn left to the y-axis and read the total harmonic voltage distortion.

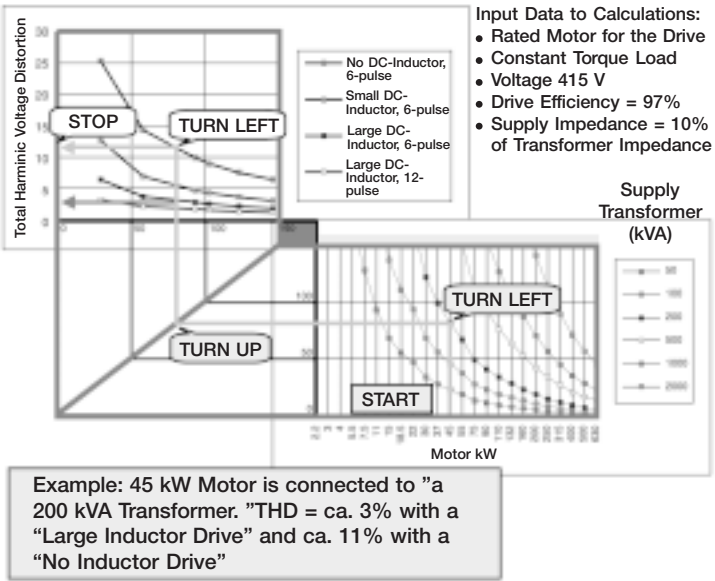


Figure 7.9 Total harmonic distortion nomogram

Results from laboratory tests with drive units from different manufacturers are shown in Figure 7.10. Drive A with large DC inductor has the lowest harmonic current distortion, drives with no inductor installed have the highest distortion.

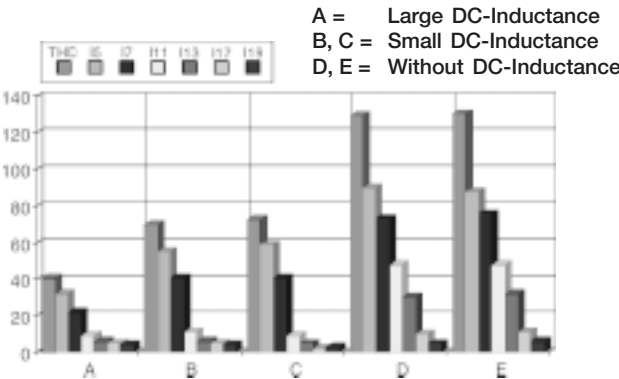


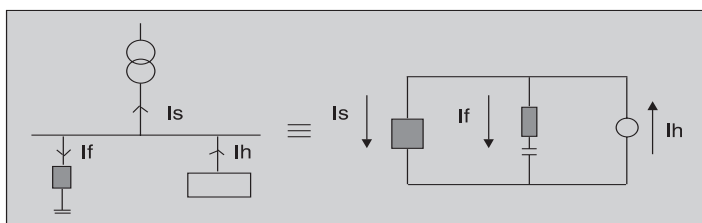
Figure 7.10. Harmonic current with different DC-Inductances

Chapter 8 - Other methods for harmonics reduction

Filtering is a method to reduce harmonics in an industrial plant when the harmonic distortion has been gradually increased or as a total solution in a new plant. There are two basic methods: passive and active filters.

8.1 Tuned single arm passive filter

The principle of a tuned arm passive filter is shown in Figure 8.1. A tuned arm passive filter should be applied at the single lowest harmonic component where there is significant harmonic generation in the system. For systems that mostly supply an industrial load this would probably be the fifth harmonic. Above the tuned frequency the harmonics are absorbed but below that frequency they may be amplified.



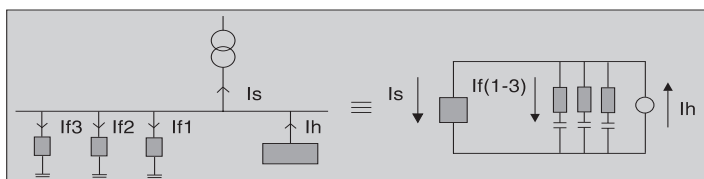
- Detuned - Single tuning frequency
- Above tuned frequency harmonics absorbed
- Below tuned frequency harmonics may be amplified
- Harmonic reduction limited by KVAR and network

Figure 8.1 Tuned single arm passive filter

This kind of filter consists of an inductor in series with a capacitor bank and the best location for the passive filter is close to the harmonic generating loads. This solution is not normally used for new installations.

8.2 Tuned multiple arm passive filter

The principle of this filter is shown in Figure 8.2. This filter has several arms tuned to two or more of the harmonic components which should be the lowest significant harmonic frequencies in the system. The multiple filter has better harmonic absorption than the one arm system.



- Capacitive below tuned frequency/Inductive above
- Better harmonic absorption
- Design consideration to amplification harmonics by filter
- Limited by KVAR and network

Figure 8.2 Tuned multiple arm passive filter

8.3 External active filter

The multiple arm passive filters are often used for large DC drive installations where a dedicated transformer is supplying the whole installation.

A passive tuned filter introduces new resonances that can cause additional harmonic problems. New power electronics technologies are resulting in products that can control harmonic distortion with active control. These active filters, see Figure 8.3, provide compensation for harmonic components on the utility system based on existing harmonic generation at any given moment in time.

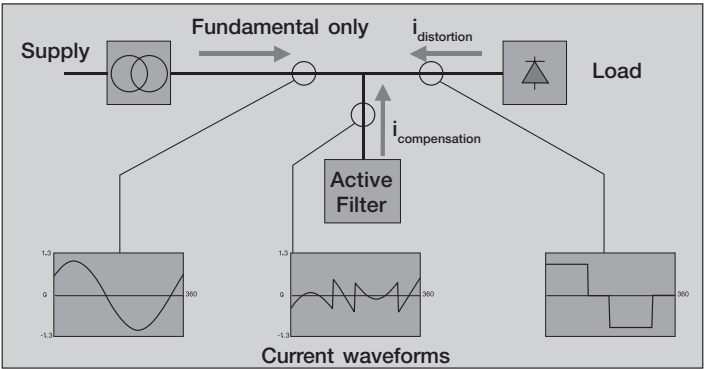


Figure 8.3 External active filter principle diagram

The active filter compensates the harmonics generated by nonlinear loads by generating the same harmonic components in opposite phase as shown in Figure 8.4. External active filters are most suited to multiple small drives. They are relatively expensive compared to other methods.

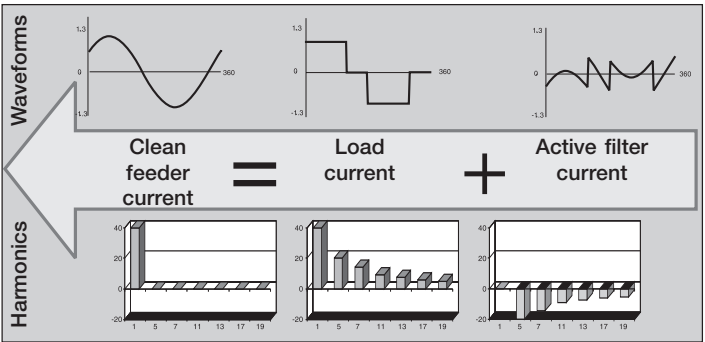


Figure 8.4 External active filter waveforms and harmonics

Chapter 9 - Summary of harmonics attenuation

There are many options to attenuate harmonics either inside the drive system or externally. They all have advantages and disadvantages and all of them show cost implications. The best solution will depend on the total loading, the supply to site and the standing distortion.

In the following tables different internal actions are compared to the basic system without inductor. The harmonic content is given with 100% load. The costs are valid for small drives. For multidrive the 12-pulse solution is quite a lot cheaper.

9.1 6-pulse rectifier without inductor

Manufacturing cost 100%
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	63%	54%	10%	6,1%	6,7%	4,8%

9.2 6-pulse rectifier with inductor

Manufacturing cost 120%. AC or DC choke added
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	30%	12%	8,9%	5,6%	4,4%	4,1%

9.3 12-pulse rectifier with polycon transformer

Manufacturing cost 200%
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	11%	5,8%	6,2%	4,7%	1,7%	1,4%

9.4 12-pulse rectifier with double wound transformer

Manufacturing cost 210%
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	3,6%	2,6%	7,5%	5,2%	1,2%	1,3%

9.5 24-pulse rectifier

Manufacturing cost 250%
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	4,0%	2,7%	1,0%	0,7%	1,4%	1,4%

**9.6 Active
IGBT rectifier**

Manufacturing cost 250%. Not significant if electrical braking is anyway needed.
Typical harmonic current components.

Fundamental	5 th	7 th	11 th	13 th	17 th	19 th
100%	2,6%	3,4%	3,0%	0,1%	2,1%	2,2%

Chapter 10 - Definitions

-
- S: Apparent power
- P: Active power
- Q: Reactive power
- Rsc: Short circuit ratio is defined as the short circuit power of the supply at PCC to the nominal apparent power of the equipment under consideration.
 $R_{sc} = S_s / S_n$.
- ω_1 : Angular frequency of fundamental component
 $\omega_1 = 2\pi f_1$, where f_1 is fundamental frequency (eg. 50Hz or 60Hz).
- n: Integer $n = 2, 3, \dots \infty$. Harmonic frequencies are defined as $\omega_n = n\omega_1$.
- I_n : RMS-value of n:th harmonic component of line current.
- Z_n : Impedance at frequency $n\omega_1$.
- %Un: Harmonic voltage component as a percentage of fundamental (line) voltage.
- THD: Total Harmonic Distortion in the input current is defined as:

$$THD = \frac{\sqrt{\sum_{h=2}^{\infty} I_h^2}}{I_1}$$

where I_1 is the rms value of the fundamental frequency current. The THD in voltage may be calculated in a similar way. Here is an example for the 25 lowest harmonic components with the theoretical values:

$$THD = \frac{\sqrt{20^2 + 14,3^2 + 9,1^2 + 7,7^2 + 5,9^2 + 5,3^2 + 4,4^2 + 4^2}}{100}$$

$$THD = 29 \%$$

PWHD: Partial weighted harmonic distortion is defined as:

$$PWHD = \sqrt{\sum_{n=14}^{40} h \left(\frac{I_h}{I_1} \right)^2}$$

PCC: Point of Common Coupling is defined in this text as such a point of utility supply which may be common to the equipment in question and other equipment. There are several definitions of PCC in different standards and even more interpretations of these definitions in literature. The definition chosen here is seen as technically most sound.

PF: Power Factor defined as $PF = P/S$ (power / volt-ampere) = $I_1 / I_s * DPF$ (With sinusoidal current PF equals to DPF).

DPF: Displacement Power Factor defined as $\cos\phi_1$, where ϕ_1 is the phase angle between the fundamental frequency current drawn by the equipment and the supply voltage fundamental frequency component.

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ABB Industry Oy

Drives

P. O. Box 184

FIN - 00381 Helsinki

Finland

Telephone +358 10 222 000

Telefax +358 10 222 2681

Internet <http://www.abb.com/motors&drives>

